TOSHIBA Photocoupler Photorelay

TLP202G

PC Card Modems
PBX
STB (Set Top Boxes)
Measurement Equipment

The Toshiba TLP202G consists of a gallium arsenide infrared emitting diode optically coupled to a photo-MOSFET in an 8-pin SOP package.

This photorelay has a characteristic of high-withstanding voltage between output pins which enables TLP202G to be applied in hook relays and dial-pulse for modems and facsimiles.

Moreover, the TLP202G is used for PCMCIA-compliant card modems because the maximum mounted height of SOP package is as small as 2.1 mm.

• 8-pin SOP (2.54SOP8): Height = 2.1 mm, Pitch = 2.54 mm

• Normally open (1-form-A) device

• Peak Off-state voltage: 350 V (min)

• Trigger LED current: 3 mA (max)

• On-state current: 110 mA (max)

• On-state resistance: 35Ω (max, t < 1 s)

• On-state resistance: 50Ω (max, continuous)

• Isolation voltage: 1500 Vrms (min)

• UL recognized: UL1557, File No.E67349

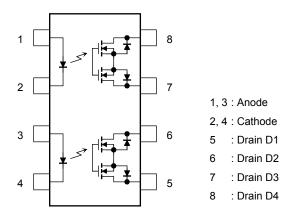
JEDEC — Unit: mm

11-10H1

Weight: 0.2 g (typ.)

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Pin Configuration (top view)



Absolute Maximum Rating (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
	Forward current	lF	50	mA	
	Forward current derating (Ta ≥ 25°C)	ΔI _F /°C	-0.5	mA/°C	
LED	Reverse voltage	V _R	5	V	
	Junction temperature	Tj	125	°C	
Detector	Off-state output terminal voltage	V _{OFF}	350	V	
	On-state current	I _{ON}	110	mA	
	Forward current derating (Ta ≥ 25°C)	Δl _{ON} /°C	-1.1	mA/°C	
	Junction temperature	Tj	125	°C	
Storage temperature range		T _{stg}	-55~125	°C	
Operating temperature range		T _{opr}	-40~85	°C	
Lead soldering temperature (10 s)		T _{sol}	260	°C	
Isolation voltage (AC, 1 min, R.H. ≦ 60%) (Note 1)		BVS	1500	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: LED pins are shorted together. Detector pins are also shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Тур.	Max	Unit
Supply voltage	V_{DD}		_	280	V
Forward current	l _F	5	10	25	mA
On-state current	I _{ON}	_	_	100	mA
Operating temperature	T _{opr}	-20	_	65	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Electrical Characteristics (Ta = 25°C)

	Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5 V	_	_	10	μА
	Capacitance	C _T	V = 0, f = 1 MHz	_	30	_	pF
Detector	Off-state current	l _{OFF}	V _{OFF} = 350 V	_	_	1	μА
Detector	Capacitance	C _{OFF}	V = 0, f = 1 MHz	_	30	_	pF

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Coupled Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}	I _{ON} = 110 mA	_	1	3	mA
Return LED current	I _{FC}	I _{OFF} = 100 μA	0.1	_	_	mA
On-state resistance	Ron	I _{ON} = 110 mA, I _F = 5 mA, t < 1 s	_	25	35	Ω
		I _{ON} = 110 mA, I _F = 5 mA		35	50	

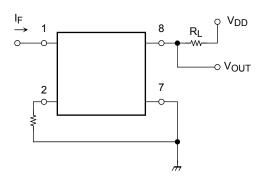
Isolation Characteristics (Ta = 25°C)

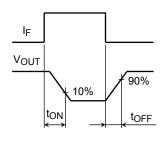
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	Cs	V _S = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≦ 60%	5×10^{10}	10 ¹⁴	_	Ω
		AC, 1 min	1500	_	_	Vrms
Isolation voltage	BV_S	AC, 1 s, in oil 3000	3000	_	VIIIIS	
		DC, 1 min, in oil	_	3000	_	Vdc

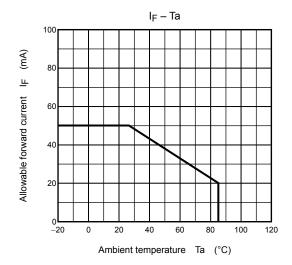
Switching Characteristics (Ta = 25°C)

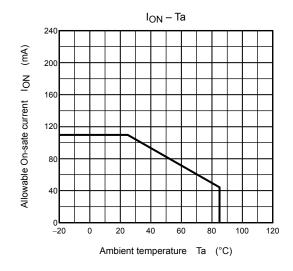
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Turn-on time	t _{ON}	$R_L = 200 \Omega$	_	0.3	1	ms
Turn-off time	t _{OFF}	$V_{DD} = 20 \text{ V}, I_F = 5 \text{ mA}$ (Note 2)	_	0.1	1	1113

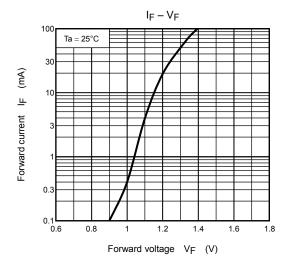
Note 2: Switching time test circuit

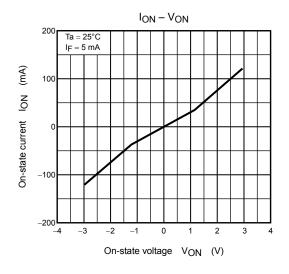


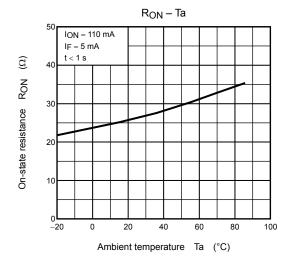


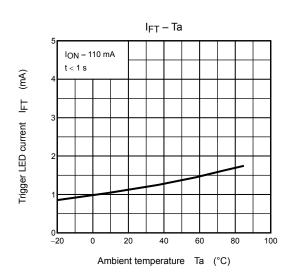




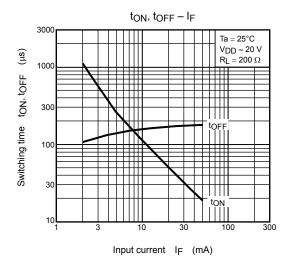


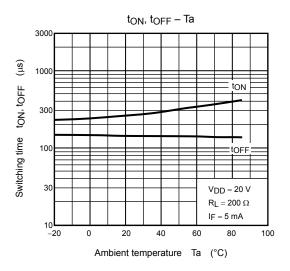


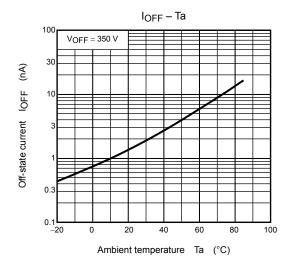




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