Regarding the change of names mentioned in the document, such as Mitsubishi Electric and Mitsubishi XX, to Renesas Technology Corp.

The semiconductor operations of Hitachi and Mitsubishi Electric were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Mitsubishi Electric, Mitsubishi Electric Corporation, Mitsubishi Semiconductors, and other Mitsubishi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

Note : Mitsubishi Electric will continue the business operations of high frequency & optical devices and power devices.

Renesas Technology Corp. Customer Support Dept. April 1, 2003



SINGLE-CHIP 8-BIT CMOS MICROCOMPUTER

DESCRIPTION

The 3812 group is the 8-bit microcomputer based on the 740 family core technology.

The 3812 group has six 8-bit timers, and an 8-channel A-D converter as additional functions.

The various microcomputers in the 3812 group include variations of internal memory size and packaging. For details, refer to the section on part numbering.

FEATURES

- Basic machine-language instructions ------ 71

- Programmable input/output ports ······· 34
- High-breakdown-voltage output ports ······ 28
- Software pull-up/pull-down resistors (P2₄-P2₇, P5₀-P5₅)

•	A-D converter
•	Zero cross detection input1 channel
•	2 Clock generating circuit
	Clock (X _{IN} -X _{OUT})internal feedback resistor
	Sub-clock (X _{CIN} -X _{COUT}) ······ without internal feedback resistor
	(connect to an external ceramic resonator or a quartz-crystal oscillator)
٠	Power source voltage
	In high-speed mode4.0 to 5.5V
	(at 6.3MHz oscillation frequency and high-speed selected)
	In middle-speed mode2.8 to 5.5V
	(at 6.3MHz oscillation frequency and middle-speed selected)
	In low-speed mode2.8 to 5.5V
	(at 32KHz oscillation frequency)
•	Power dissipation
	In high-speed mode
	(at 6.3MHz oscillation frequency)
	In low-speed mode
	(at 32kHz oscillation frequency)
•	Operating temperature range -10 to $+85^{\circ}$

APPLICATIONS

VCRs, tuners, musical instruments, office automation, etc.



MITSUBISHI MICROCOMPUTERS

3812 Group

SINGLE-CHIP 8-BIT CMOS MICROCOMPUTER





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SINGLE-CHIP 8-BIT CMOS MICROCOMPUTER

PIN DESCRIPTION

Pin	Name	Function		
			Function except a port function	
V _{cc} , V _{ss}	Power source	- Apply voltage of 4.0 to 5.5V to V _{CC} , and 0V to V _{SS} .		
VEE	Pull-down power source input	Applies voltage supplied to pull-down resistors of ports P0, P1, and P20-P23.		
VREF	Analog reference voltage	Reference voltage input pin for A-D converter		
AV _{SS}	Analog power source	Analog power source input pin for A-D converter Connect AV _{SS} to V _{SS} .		
RESET	Reset input	Reset input pin for active "L"		
X _{IN}	Clock input	 Input and output signals for the internal clock generating circuit. Feedback resistor is built in between X_{IN} pin and X_{OUT} pin. Connect a ceramic resonator or a quartz-crystal oscillator between the X_{IN} and X_{OUT} pins to set the oscillation frequency. If an external clock is used, connect the clock source to the X_{IN} pin and leave the X_{OUT} pin open. This clock is used as the oscillating source of system clock. 		
X _{out}	Clock output			
P00-P07	Output port P0	 8-bit output port Each port builds in pull-down resistor between the output and the V_{EE} pin. The high-breakdown-voltage p-channel open-drain output At reset these pins are set to the V_{EE} pin level. 		
P10-P17	Output port P1			
P20-P23	Output port P2	4-bit output port with the same function as port P0.		
P2₄-P2 ₇	I/O port P2	 4-bit I/O port I/O direction register allows each pin to be individually programmed as either input or output. At reset this port is set to input mode. Pull-up/pull-down register and I/O direction register allow each pin to be programmed as pull-down. TTL input level CMOS 3-state output 		
P3 ₀ -P3 ₇	I/O port P3	 8-bit I/O port with the same function as port P2₄-P2₇ CMOS compatible input level The high-breakdown-voltage P-channel open-drain. 		
P4 ₀ /INT ₀ , P4 ₁ /INT ₁ / ZCR	Input port P4	• 2-bit input port. • CMOS compatible input level	External interrupt input pins A zero cross detection circuit input pin (P4 ₁)	
P42/INT2	I/O port P4	6-bit CMOS I/O port with the same function as port P24- p2		
P4 ₃ /PWM		P27 • CMOS compatible input level • CMOS 2 state subsut	A PWM output pin (Timer output pin)	
P4 ₄ /CNTR ₀ , P4 ₅ /CNTR ₁		CMOS 3-state output	Timer 2, Timer 4 input pins	
Р4 ₆ /Т1 _{ОUT} , Р4 ₇ /ТЗ _{ОUT}			Timer 1, Timer 3 output pins	



SINGLE-CHIP 8-BIT CMOS MICROCOMPUTER

PIN DESCRIPTION (Continued)

Pin	Name	Function		
			Function except a port function	
P5 ₀ /S _{IN} , P5 ₁ /S _{OUT} , P5 ₂ /S _{CLK} , P5 ₃ /S _{RDY}	I/O port P5	 8-bit CMOS I/O port with the same function as port P2₄-P2₇ Keep the input voltage of this port between 0V and V_{cc}. The pull-up/pull-down register and 1/O direction register allow each pin to be programmed as pull-up. CMOS compatible input level N-channel open-drain output 	Serial I/O pins	
P5₄, P5₅		 2-bit CMOS I/O port with the same function as port P2₄-P The pull-up/pull-down register and I/O direction register CMOS compatible input level CMOS 3-state output 	-	
P6 ₀ /AN ₀ - P6 ₇ /AN ₇	I/O port P6	 8-bit CMOS I/O port with the same function as port P2₄-P2₇ CMOS compatible input level CMOS 3-state output 	A-D converter input pins	
Р7 ₀ /Х _{СОИТ} , Р7 ₁ /Х _{СІМ}	I/O port P7	 2-bit CMOS I/O port with the same function as port P2₄- P2₇ CMOS compatible input level CMOS 3-state output 	An I/O pin for the internal sub-clock generating circuit (connect a ceramic resonator or a quartz-crystal oscillator)	



SINGLE-CHIP 8-BIT CMOS MICROCOMPUTER

PART NUMBERING





SINGLE-CHIP 8-BIT CMOS MICROCOMPUTER

GROUP EXPANSION

Mitsubishi plans to expand the 3812 group as follows:

(1) Support for mask ROM, One Time PROM, and EPROM versions

ROM/PROM size ······	······ 8K to 48K bytes
RAM size ·····	

(2) Packages
 64P4B ········· Shrink plastic molded DIP
 64P6N-A ········· Plastic molded QFP
 64S1B-E ········ Shrink ceramic DIP (EPROM version)
 64D0 ······ Ceramic LCC (EPROM version)



Currently supported products are listed below.

As of May 1996

Product	(P) ROM size (bytes) ROM size for User in ()	RAM size (bytes)	Package	Remarks
M38122M2-XXXSP	8192		64P4B	Mask ROM version
M38122M2-XXXFP	(8062)	384	64P6N-A	Mask ROM version
M38122M4-XXXSP			64P4B	Mask ROM version
M38122M4-XXXFP	16384		64P6N-A	Mask ROM version
M38123M4-XXXSP	(16254)	512	64P4B	Mask ROM version
M38123M4-XXXFP			64P6N-A	Mask ROM version
M38123M6-XXXSP	24576		64P4B	Mask ROM version
M38123M6-XXXFP	(24446)		64P6N-A	Mask ROM version
M38127M8-XXXSP	32768	32768		Mask ROM version
M38127M8-XXXFP	(32638)	1024	64P6N-A	Mask ROM version
M38127EC-XXXSP			64P4B	One Time PROM version
M38127EC-XXXFP			64P6N-A	One Time PROM version
M38127ECSP	49152		64P4B	One Time PROM version (blank)
M38127ECFP	(49022)		64P6N-A	One Time PROM version (blank)
M38127ECSS	3127ECSS		64S1B-E	EPROM version
M38127ECFS			64D0	EPROM version



SINGLE-CHIP 8-BIT CMOS MICROCOMPUTER

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Notes regarding these materials

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REVISION HISTORY

3812 GROUP USER'S MANUAL

Rev.	Date		Description	
	24.0	Page	Summary	
1.0	07/10/02	- 3 -	The first edition is issued.	