

NE5532

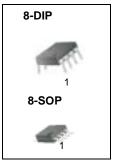
Dual Operational Amplifier

Features

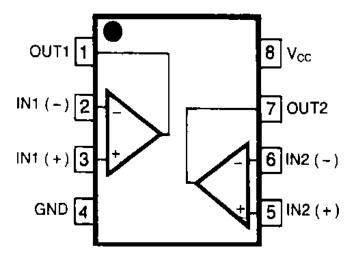
- Internal Frequency Compensation
- Slew Rate: 8V/μs
- Input Noise Voltage: $8nV/\sqrt{Hz}$ (fo = 30Hz)
- Full Power Bandwidth: 140KHz

Description

The NE5532 is a internally compensated dual low noise OP-AMP. The high small signal and power bandwidth provides superior performance in high quality AMP, all control circuits, and telephone applications.



Internal Block Diagram



Absolute Maximum Ratings

Parameter	Symbol	NE5532	Unit
Power Supply Voltage	Vcc	±22	V
Differential Input Voltage	V(DIFF)	±13	V
Input Voltage	VI	Supply Voltage	V
Power Dissipation, T _A = 25°C 8-DIP 8-SOP	PD	1100 500	mW
Operating Temperature Range	TOPR	0 ~ + 70	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-Ambient Max. 8-DIP 8-SOP	Rθja	110 250	°C/W

Electrical Characteristics

 $(VCC=15V, VEE= - 15V, TA = 25^{\circ}C)$

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input Offset Voltage	VIO	-	-	0.5	4.0	mV
Input Offset Current	ΙΙΟ	-	-	10	150	nA
Input Bias Current	IBIAS	-	-	200	800	nA
Supply Current	Icc	-	-	6.0	16	mA
Input Voltage Range	VI(R)	-	±12	±13	-	V
Common Mode Rejection Range	CMRR	T _A = 25 °C	70	100	-	dB
Power Supply Rejection Ratio	PSRR	T _A = 25 °C	80	100	-	dB
Output Voltage Swing	VO(P-P)	R _L ≥ 600Ω	±12	±13	-	V
Input Resistance	Rı	T _A = 25 °C	30	300	-	ΚΩ
Short Circuit Current	Isc	-	-	38	-	mA
Overshoot	OS	R _L =600Ω, C _L =100pF	-	10	20	%
Voltage Gain	Gv	f = 10KHz	2	2.2	-	V/mV
Gain Bandwidth Product	GBW	$C_L = 100 pF$, $R_L = 600 \Omega$	8	10	-	MHz
Slew Rate	SR	$R_L = 1K$, $C_L = 100pF$, $R_L = 600\Omega$	6	8.0	-	V/μs
Input Noise Voltage	e _N	fO = 30Hz fO = 1KHz	ı	8.0 5.0	-	nV/√Hz

Typical Performance Characteristics

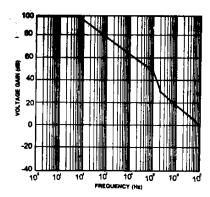


Figure 1. Open Loop Frequency Response

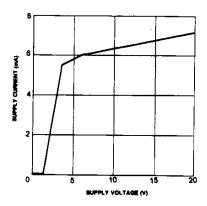


Figure 3. Supply Current vs Supply Voltage

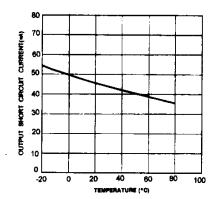


Figure 5. Output Circuit Current vs Temperature

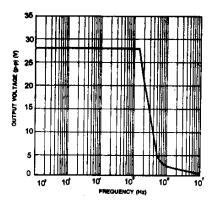


Figure 2. Large Signal Frequency Response

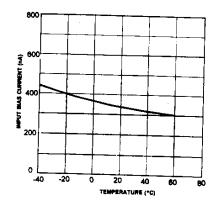


Figure 4. Input Bias Current vs Temperature

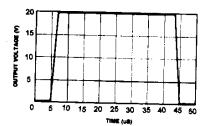
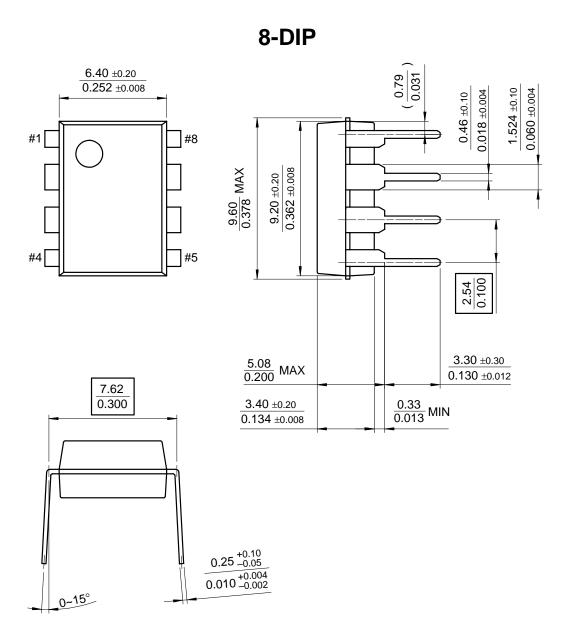


Figure 6. Slew Rate

Mechanical Dimensions

Package

Dimensions in millimeters

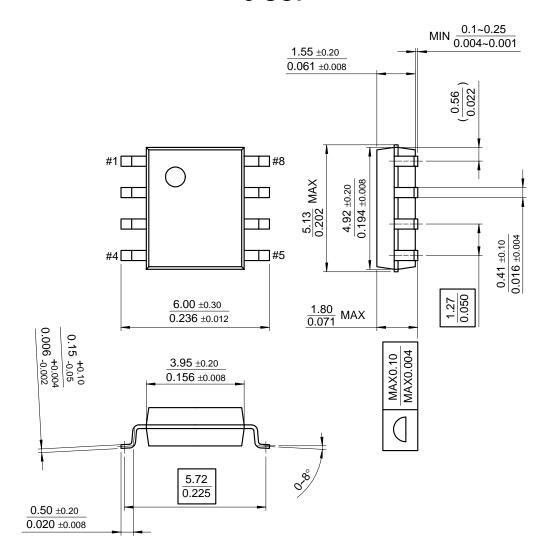


Mechanical Dimensions (Continued)

Package

Dimensions in millimeters

8-SOP



Ordering Information

Product Number	Package	Operating Temperature
NE5532N	8-DIP	0 ~ +70°C
NE5532D	8-SOP	0 ~ +70 C

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