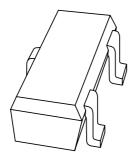
DISCRETE SEMICONDUCTORS

DATA SHEET



2PD601AWNPN general purpose transistor

Preliminary specification

2002 Jun 26





2PD601AW

FEATURES

- High collector current (max. 100 mA)
- Low collector-emitter saturation voltage (max. 500 mV).

APPLICATIONS

· General purpose switching and amplification.

DESCRIPTION

NPN transistor in an SC-70 (SOT323) plastic package. PNP complement: 2PB709AW.

MARKING

TYPE NUMBER	MARKING CODE(1)
2PD601AQW	*6D
2PD601ARW	*6E
2PD601ASW	*6F

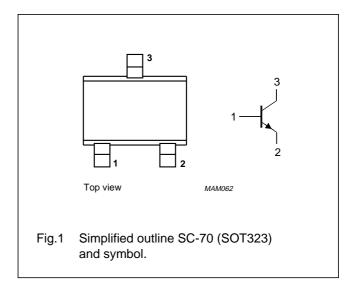
Note

1. * = -: made in Hong Kong.

* = t: made in Malaysia.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	60	V
V _{CEO}	collector-emitter voltage	open base	_	50	V
V _{EBO}	emitter-base voltage	open collector	_	6	V
I _C	collector current (DC)		_	100	mA
I _{CM}	peak collector current		_	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	200	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. For mounting conditions, see "Thermal considerations and footprint design for SOT323 in the General Part of Data Handbook SC18".

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	625	K/W

Note

1. For mounting conditions, see "Thermal considerations and footprint design for SOT323 in the General Part of Data Handbook SC18".

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	I _E = 0; V _{CB} = 60 V	_	10	nA
		I _E = 0; V _{CB} = 60 V; T _j = 150 °C	_	5	μΑ
I _{EBO}	emitter-base cut-off current	I _C = 0; V _{EB} = 5 V	_	10	nA
h _{FE}	DC current gain	I _C = 100 mA; V _{CE} = 2 V; note 1	90	_	
	DC current gain	I _C = 2 mA; V _{CE} = 10 V			
	2PD601AQW		160	260	
	2PD601ARW		210	340	
	2PD601ASW		290	460	
V _{CEsat}	collector-emitter saturation voltage	I _C = 100 mA; I _B = 10 mA; note 1	_	500	mV
C _c	collector capacitance	I _E = i _e = 0; V _{CB} = 10 V; f = 1 MHz	_	3.5	pF
f _T	transition frequency	I _C = 2 mA; V _{CE} = 10 V;			
	2PD601AQW	f = 100 MHz	100	_	MHz
	2PD601ARW		120	_	MHz
	2PD601ASW		140	_	MHz

Note

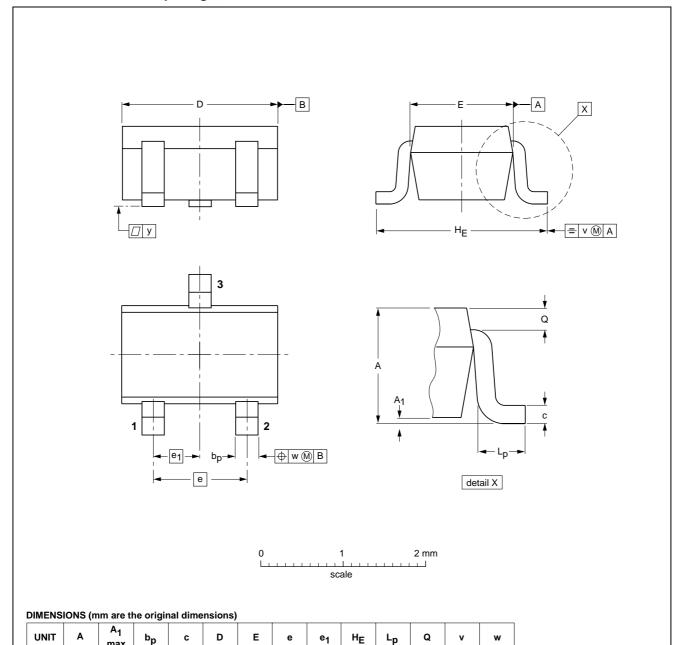
1. Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

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PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT323



OUTLINE		REFERENCES		EUROPEAN	ICCUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT323			SC-70			97-02-28

0.65

0.45 0.15 0.23 0.13

0.2

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0.4 0.3 0.25 0.10 2.2 1.8 1.35 1.15

1.1 0.8

mm

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NPN general purpose transistor

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DATA SHEET STATUS

DATA SHEET STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITIONS
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Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Changes will be communicated according to the Customer Product/Process Change Notification (CPCN) procedure SNW-SQ-650A.

Notes

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- 2. The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.

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NOTES

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NOTES

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