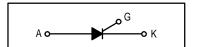
Silicon Controlled RectifiersReverse Blocking Thyristors

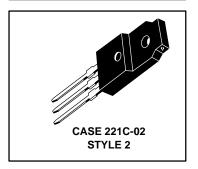
... designed primarily for half-wave ac control applications, such as motor controls, heating controls and power supply crowbar circuits.

- Glass Passivated Junctions with Center Gate Fire for Greater Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Constructed for Low Thermal Resistance, High Heat Dissipation and Durability
- Blocking Voltage to 800 Volts
- 300 A Surge Current Capability
- · Insulated Package Simplifies Mounting



MCR225FP Series

ISOLATED SCRs 25 AMPERES RMS 600 thru 800 VOLTS



MAXIMUM RATINGS ($T_J = 25^{\circ}C$ unless otherwise noted.)

Rating	Symbol	Value	Unit
Peak Repetitive Forward and Reverse Blocking Voltage ⁽¹⁾ (T _J = -40 to +125°C, Gate Open) MCR225-8FP MCR225-10FP	VDRM VRRM	600 800	Volts
On-State RMS Current ($T_C = +70^{\circ}C$) Full Cycle Sine Wave 50 to 60 Hz ⁽²⁾	I _T (RMS)	25	Amps
Peak Non-repetitive Surge Current (One Full Cycle, 60 Hz, T _C = +70°C) Preceded and followed by rated current	ITSM	300	Amps
Circuit Fusing (t = 8.3 ms)	l ² t	375	A ² s
Peak Gate Power (T _C = +70°C, Pulse Width = 10 μs)	P _{GM}	20	Watts
Average Gate Power (T _C = +70°C, t = 8.3 ms)	PG(AV)	0.5	Watt
Peak Gate Current (T _C = +70°C, Pulse Width = 10 μs)	IGМ	2	Amps
RMS Isolation Voltage (T _A = 25°C, Relative Humidity ≤ 20%)	V _(ISO)	1500	Volts
Operating Junction Temperature Range	TJ	-40 to +125	°C
Storage Temperature Range	T _{stg}	-40 to +125	°C

^{1.} V_{DRM} and V_{RRM} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

REV 1



^{2.} The case temperature reference point for all T_C measurements is a point on the center lead of the package as close as possible to the plastic body.

MCR225FP Series

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	1.5	°C/W
Thermal Resistance, Case to Sink	R _θ CS	2.2 (typ)	°C/W
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	60	°C/W

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Peak Forward Blocking Current (V_D = Rated V_{DRM} , Gate Open) $T_J = 25^{\circ}C$ $T_J = 125^{\circ}C$	IDRM		_	10 2	μA mA
Peak Reverse Blocking Current $(V_R = Rated V_{RRM})$ $T_J = 125^{\circ}C$	IRRM	_	_	2	mA
Forward "On" Voltage ⁽¹⁾ (I _{TM} = 50 A)	VTM	_	_	1.8	Volts
Gate Trigger Current (Continuous dc) (Anode Voltage = 12 Vdc, R _L = 100 Ohms)	lGT	_	_	40	mA
Gate Trigger Voltage (Continuous dc) (Anode Voltage = 12 Vdc, R _L = 100 Ohms)	V _{GT}	_	0.8	1.5	Volts
Gate Non-Trigger Voltage (Anode Voltage = Rated V _{DRM} , R _L = 100 Ohms, T _J = 125°C)	V _{GD}	0.2	_	_	Volts
Holding Current (Anode Voltage = 12 Vdc)	Ιн	_	20	40	mA
Turn-On Time (I _{TM} = 25 A, I _{GT} = 40 mAdc)	^t gt	_	1.5	_	μs
Turn-Off Time (V _{DRM} = Rated Voltage) (I _{TM} = 25 A, I _R = 25 A) (I _{TM} = 25 A, I _R = 25 A, T _J = 125°C)	tq	_	15 35	_	μs
Critical Rate-of-Rise of Off-State Voltage (Gate Open, V_D = Rated V_{DRM} , Exponential Waveform)	dv/dt	_	100	_	V/μs

^{1.} Pulse Test: Pulse Width = 1 ms, Duty Cycle \leq 2%.

TYPICAL CHARACTERISTICS

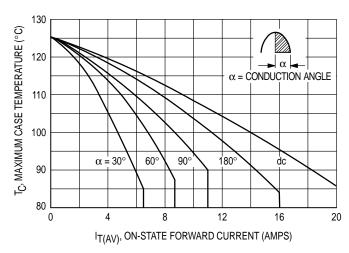


Figure 1. Average Current Derating

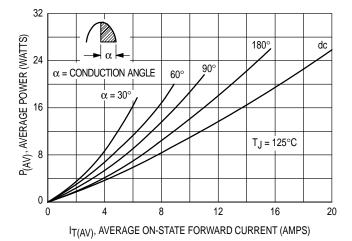


Figure 2. Maximum On-State Power Dissipation

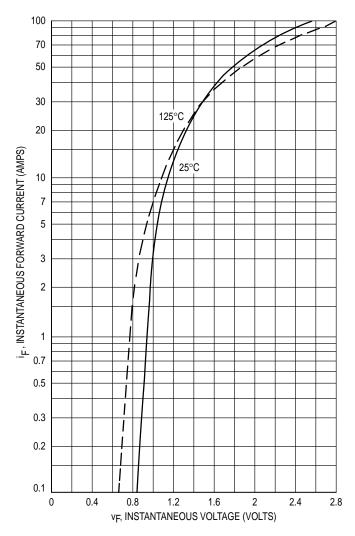


Figure 3. Maximum Forward Voltage

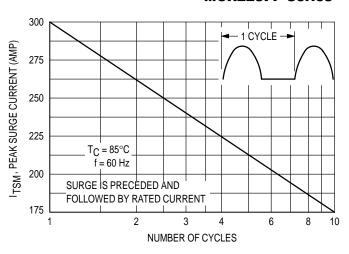


Figure 4. Maximum Non-Repetitive Surge Current

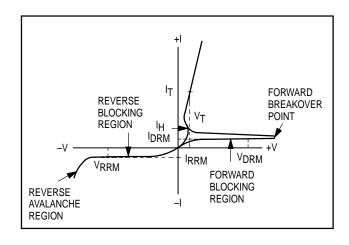


Figure 5. Characteristics and Symbols

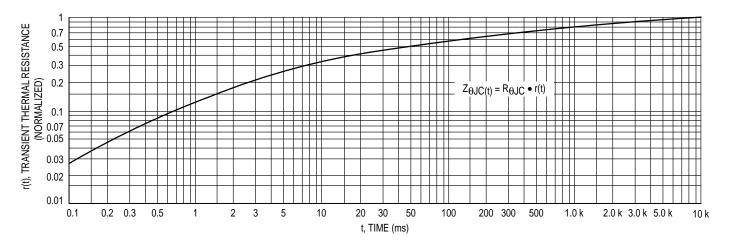


Figure 6. Thermal Response

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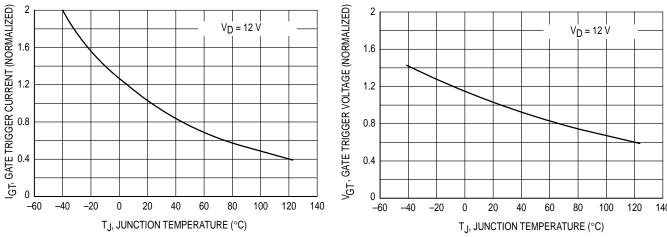


Figure 7. Gate Trigger Current versus Temperature

Figure 8. Gate Trigger Voltage versus Temperature

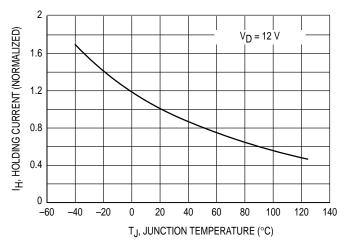
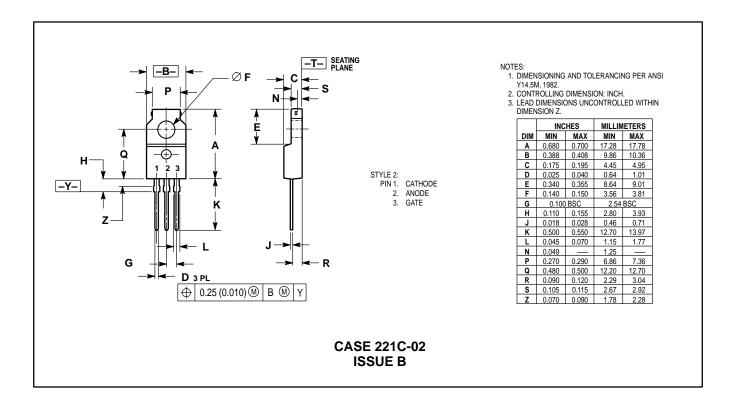


Figure 9. Holding Current versus Temperature

PACKAGE DIMENSIONS



MCR225FP Series

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