



				SPEC. NO.	TQ3C-	8EACO-E1CWG25	-01	
				DATE	Au	gust 26, 2004		
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		FOR						
		<u>TYPE:</u>	<u>KHS057QV1</u>	<u> </u>	1			
-			CONTENTS	5				
	2. C 3. M 4. A 5. E 6. O 7. C 8. Ir	pplication onstruction and Outlin echanical Specification bsolute Maximum Rating lectrical Characterist ptical Characteristics inound Plock Pragram terface Signals	ons gs vics	T	ssued			
	10. Da	iterface Timing Chart ita and Screen				SEP. 22.2004		
	12. Su	pput Timing Characteri pply Voltage Sequence	Condition	1 <u>-</u>	KHI KHI			
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Caution

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices in the areas of audio control, office automation, industrial control, home appliances, etc. The modules should not be used in applications where module failure could result in physical harm or loss of life, and Kyocera expressly disclaims any and all liability relating in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, losses, damages, liabilities, awards, costs, and expenses, including legal fees, resulting from or arising out of Customer's use, or sale for use, of Kyocera modules in applications.
- 3. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

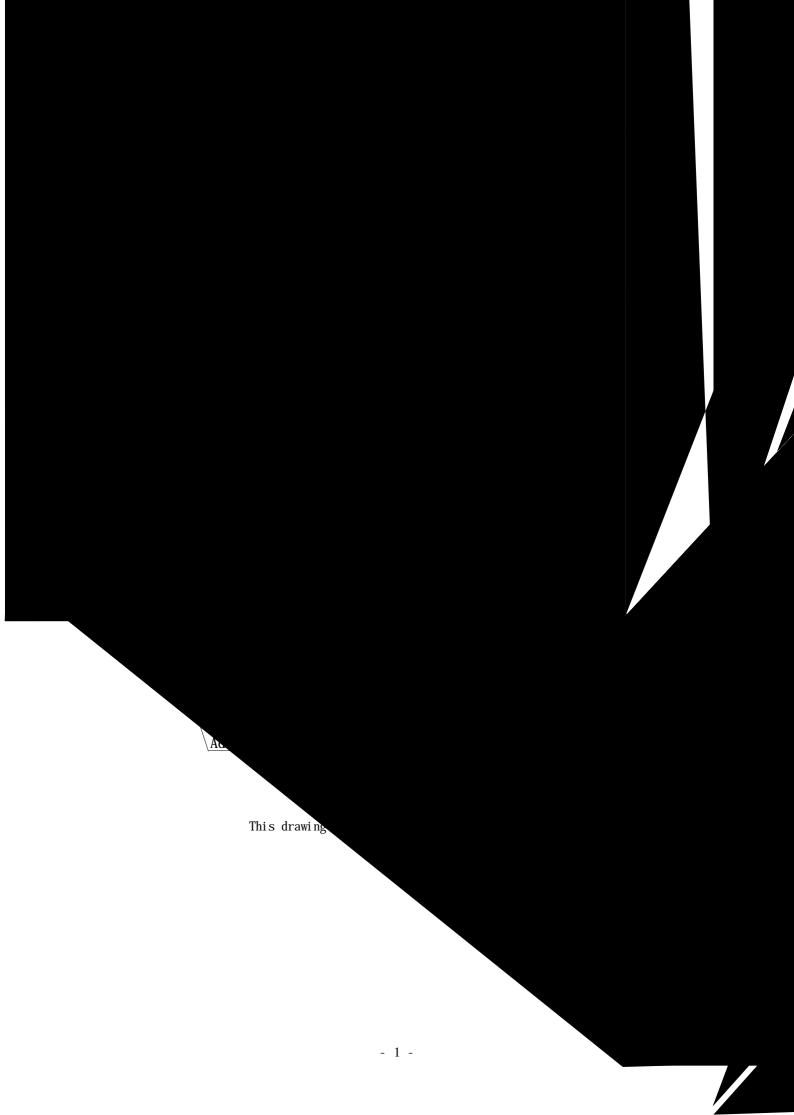


				Revision	Record		
Dat		De	signed	by: Engineeri	ng Dept.	Confirmed	by: QA Dept.
Dat	.e	Prepa	red	Checked	Approved		
Aug. 26,	2004	y. Janaz	aki	W. Yomo	M.FujiTani	y. yoshid	S. Hoyashi
Rev. No.	Da	te	Page	v	Descr	iptions	
01	Aug. 20	6, 2004	4	\sim Changed "	l Charactristic LCD driving vo igh frequency .=25°C."	ltage.″	
			11	8-2. CFL ~Changed c ~Delete ″L	omment "SYMBOL" EVEL."	"and"DESCRIPT	[ON".
			14	11. Input Ti \sim Change ch	ming Charactri art "FRM."	stics	
	19			∼Add comme 16-3. LCD op	lation of the b nt "8. This ky eration omment "2. Adju	ocera LCD	

Revision Record

DENSITRON°

DISPLAYS







Mechanical Specifications

I TEM	SPECI FI CATI ON	UNI T
Outline dimensions	154.6 (W) \times 114.8 (H) \times 8.5 (D)	mm
Effective viewing area	118.18 (W) × 89.38 (H)	mm
Dot number	$(320 \times R. G. B)$ (W) \times 240 (H)	Dots
Dot size	0.10 (W) × 0.34 (H)	mm
Dot pitch	0.12 (W) × 0.36 (H)	mm
Display color *1	White *2	
Base color *1	Bl ack *2	
Mass	200	g

*1 Due to the characteristics of the LC material, the color vary with environmental temperature. *2 Negative-type display

Display data "H" :R.G.B Dots ON : White Display data "L" :R.G.B Dots OFF : Black

Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

I TEM	SYMBOL	MIN.	MAX.	UNI T
Supply voltage for logic	VDD	0	7.0	V
Supply voltage for LCD driving	VEE	0	33.0	V
Input signal voltage *1	Vi n	0	VDD	V

*1 Input signal : CP, LOAD, FRM, DISP, DO D7





4-2. Environmental absolute maximum ratings

I TEM		SYMBOL	MI N	MAX	UNI T
Operating temperature	*1	Тор	0	50	
Storage temperature	*2	Т	- 20	60	
Operating humidity	*3	Нор	10	*4	RH
Storage humidity	*3	Н	10	*4	RH
Vi brati on			*5	*5	
Shock			*6	*6	

*1 LCD's display quality shall not be guaranteed at the temperature range of : below 0 and upper 40 $\,$

*2 Temp. = -20 48 h , Temp = 60 168 h Store LCD panel at normal temperature/humidity. Keep it free from vibration and shock. LCD panel that is kept at low or high temperature for a long time can be defective due to the other conditions, even if the temperature satisfies standard.

*3 Non-condensation.

*4 Temp. 40 , 85 RH Max. Temp. 40 , Absolute Humidity shall be less than 85 RH at 40 .

*5

Frequency	10 55 Hz	Converted to acceleration value :
Vibration width	0.15 mm	$\begin{array}{c} \text{acceleration value} \\ 0.3 9\text{m/s}^2 \end{array}$
Interval	10-55-10)Hz 1 minute

2 hours in each direction $\mbox{ X/Y/Z}$ (6 hours as total) EIAJ ED-2531.

*6 Acceleration: 490m/s^2 Pulse width : 11 ms 3 times in each direction : $\pm \text{X/}\pm \text{Y/}\pm \text{Z}$. EIAJ ED-2531.



5 Electrical Characteristics

			VDD	= $+5.0V \pm$	5%, Temp.	= 0 50
I TEM	SYMBOL	CONDI TI ON	MIN.	TYP.	MAX.	UNI T
Supply voltage for logic	VDD		4.75	5.00	5.25	V
LCD driving voltage *1	Von	0	(25.0)	(26.0)	(27.0)	V
	Vop= VEE	25	(24.6)	(25.6)	(26.6)	V
		50	(23.6)	(24.6)	(25.6)	V
Input voltage	Vin	"H" level	0. 8VDD		VDD	V
		"L" level	0		0. 2VDD	V
Clock frequency	ср		2.02	2.16	16.0	MHz
Frame frequency *2			70	75		Hz
Current consumption for logic	I DD			(3.0)	(4.5)	mA
Current consumption for LCD driving	I EE	*3		(7.5)	(11.3)	mA
Power consumption	Pdi sp			(210)	(330)	mW

 *1 Maximum contrast ratio is obtained by adjusting the LCD supply voltage(Vop= VEE) for driving LCD.

- *2 In consideration of display quality, it is recommended that frame frequency is set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency values: Generally, as frame and clock frequencies become higher, current consumption will get bigger and display quality will be degraded.
- *3 Display high frequency pattern, (see below). VDD = 5.0V , Vop = VEE , = 75 Hz , fcp = 2.16MHz , Temp. = 25 Pattern:





6mm , Temp. = 25

Optical Characteristics

6-1. Reflective mode

					01		•
ITEN	1	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNI T
Response time	Rise	Tr	= =0°		(200)	(300)	ms
UIME	Down	Td	= =0°		(150)	(250)	ms
Contrast ratio		CR	= =0°	(4.0)	(8.0)		
Reflectance				(2.5)	(5.0)		%

Measuring Spot =

Optimum contrast is obtained by adjusting the LCD driving voltage(Vop) while at the viewing angle of $= - 0^{\circ}$.

6-2. Transmimissive mode

5-2. ITalishi hi SS	sive libue				Measur	ing Spot =	6mm , Tem	р. = 25
I TEM		SYMBOL	CONDI TI ON		MIN.	TYP.	MAX.	UNI T
Response	Rise	Tr	=	=0°		(200)	(300)	ms
time	Down	Td	=	=0°		(150)	(250)	ms
Vi ewi ng angl e	range		=0		(-30)		(40)	deg.
			CR 2 =0		(-50)		(50)	deg.
Contrast ratio	Contrast ratio		= =0°		(15.0)	(30.0)		
Brightness (II	Brightness (IL=5mA)				(90)	(130)		cd/
Chromaticity coordinates	Red	х		=0°	(0.39)	(0. 44)	(0.49)	
coordinates		у	=	=0	(0.28)	(0.33)	(0.38)	
	Green	х		=0°	(0.27)	(0.32)	(0.37)	
		у	=	=0	(0.36)	(0. 41)	(0.46)	
	Bl ue	х		٥°	(0.17)	(0. 22)	(0. 27)	
		у	=	=0°	(0.15)	(0. 20)	(0.25)	
	White	х		=0°	(0.26)	(0.31)	(0.36)	
		у	=	=0	(0.26)	(0.31)	(0.36)	

Optimum contrast is obtained by adjusting the LCD driving voltage(Vop) while at the viewing angle of $= = 0^{\circ}$.





6-3. Definition of Reflectance

 $(Reflectance) = \frac{Measured Reflection Brightness}{Reflection Brightness against Standard White Board} \times 100[\%]$

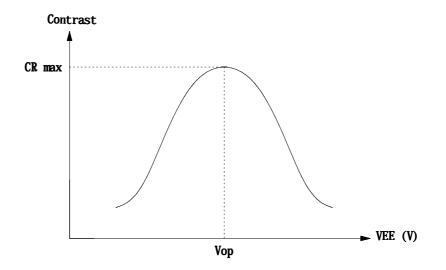
6-4. Definition of Contrast (Reflective Mode)

(Contrast) = Reflectance at all pixels "White"
 Reflectance at all pixels "Black"

6-5. Definition of Contrast (Transmissive Mode)

(Contrast) = Brightness at all pixels "White" Brightness at all pixels "Black"

6-6. Definition of Vop

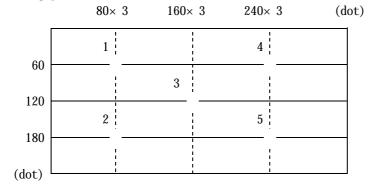






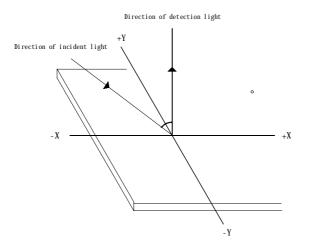


6-9. Measuring points



- 1) Rating is defined as the average brightness inside the viewing area.
- 2) 30 minutes after CFL is turned on. (Ambient Temp.=25 $\,$)
- The inverter should meet the eccentric conditions;
 Sine, symmetric waveform without spike in positive and negative.
- 4) Measuring Inverter : PH-BLC-08-K3(HITACHI MEDIA ELECTRONICS)

6-10. Measurement method of reflectance

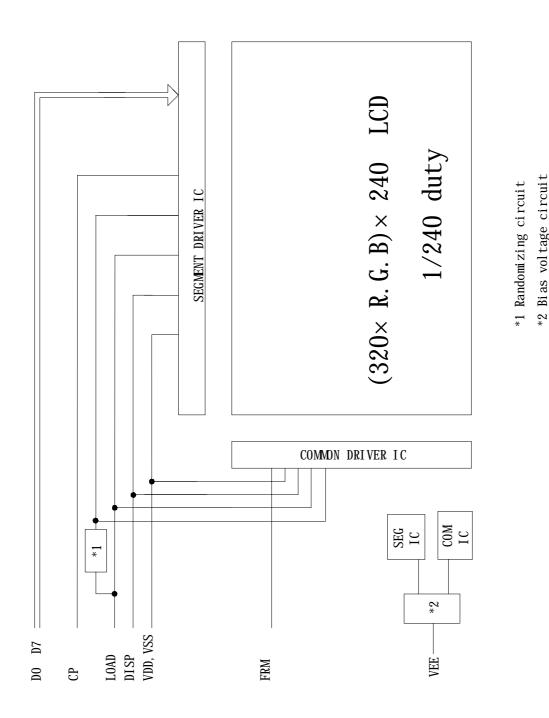






Bias voltage circuit

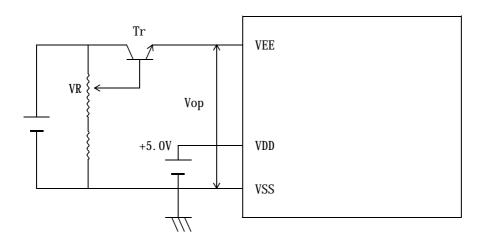
. Circuit Block Diagram







7-1. Power supply







.Interface signals

8-1. LCD

CNI : 53261-1510(Molex)	CN1	:	53261-1510(Molex)
-------------------------	-----	---	-------------------

PIN NO.	SYMBOL	DESCRI PTI ON	LEVEL
1	FRM	Synchronous signal for driving scanning line	Н
2	LOAD	Data signal latch clock	H L
3	СР	Data signal shift clock	H L
4	DI SP	Display control signal	H(ON), $L(OFF)$
5	VDD	Power supply for logic	
6	VSS	GND	
7	VEE	Power supply for LCD	
8	D7		
9	D6	Display data	
10	D5		
11	D4		H(ON), L(OFF)
12	D3		
13	D2		
14	D1		
15	DO		

Recommended matching connector 51021-1500(Molex)

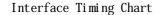
This pin assignment is the reverse of what Molex defined. Remember that for you designing.

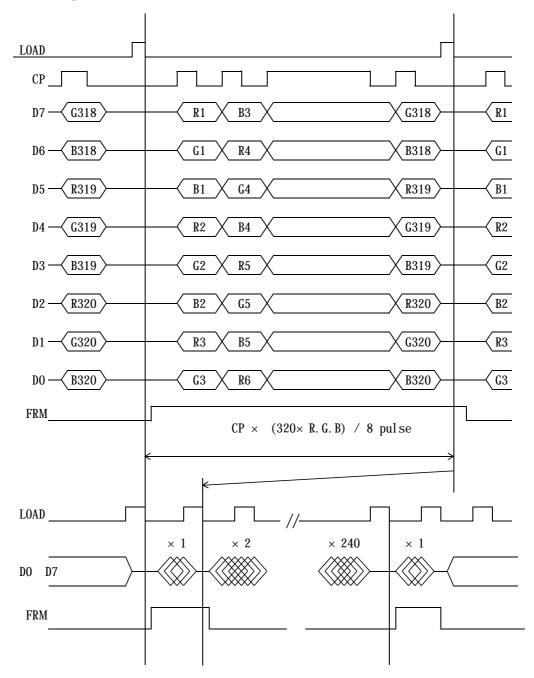
8-2. CFL

PIN No	SYMBOL	DESCRI PTI ON
1	HOT	Inverter output high voltage side
2	NC	No connect
3	COLD	Inverter output low voltage side
	connector ed matching cor	BHR-03VS-1 (JST) nnector SMD2-(8.0)B-BHS-1 (JST)









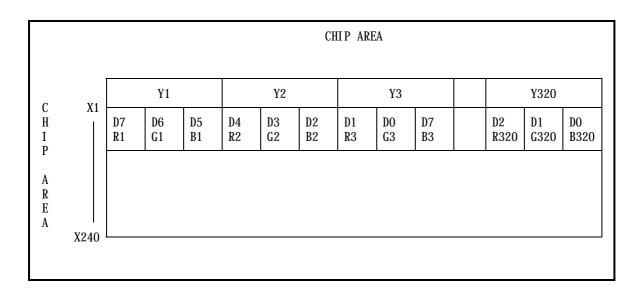
* The cycle of load signal should be stable and continuously applied without interruption.

* The above-mentioned timing chart shows a reference to set up a LCD module, not an electrical rating.



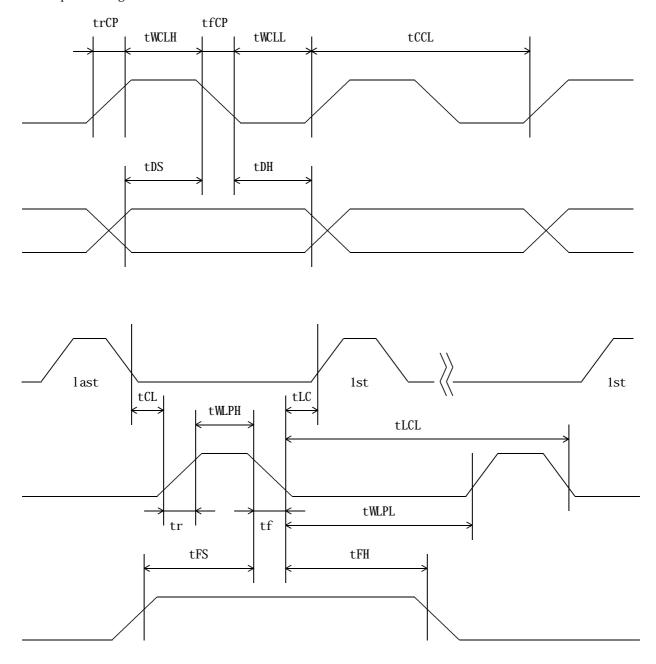


Data and Screen









Input Timing Characteristics





11-1. Switching characteristics

	Input	characteristi	cs VDD = +	$5.0V \pm 5\%$	Гетр. = 25
I TEM		SYMBOL	MIN.	MAX.	UNI T
CP Cycle	*1, *2	tCCL	62		ns
CP "H" Pulse Width	*2	tWCLH	25		ns
CP "L" Pulse Width	*2	tWCLL	25		ns
CP Rise Up Time	*2	trCP		30	ns
CP Fall Down Time	*2	tfCP		30	ns
Data Set Up Time		tDS	20		ns
Data Hold Time		tDH	15		ns
LOAD "H" Pulse Width		tWLPH	50		ns
LOAD "L" Pulse Width		tWLPL	370		ns
LOAD Cycle	*3	tLCL	420		ns
CP LOAD Delay Time		tCL	0		ns
LOAD CP Delay Time	*4	tLC	120-tWLPH		ns
Input Signal Rise Up Tim	е	tr		30	ns
Input Signal Fall Down T	ime	tf		30	ns
FRM Data Set Up Time		tFS	100		ns
FRM Data Hold Time		tFH	30		ns

*1 CP Cycle is adjust so that FRM signal is 75Hz. *2 The formula of condition trCP + tfCP tCCL - (tWCLH + tWCLL) trCp, tfCp 30ns Please use on condition that , are filled. *3 LOAD Cycle is const. *4 tLC 0



<u>DO NOT</u> apply DC voltage to the LCD panel. DC v reactions and reduce LCD life. Always follow t input signal second, VEE third and finally DIS CMOS LSI latch up as shown below.

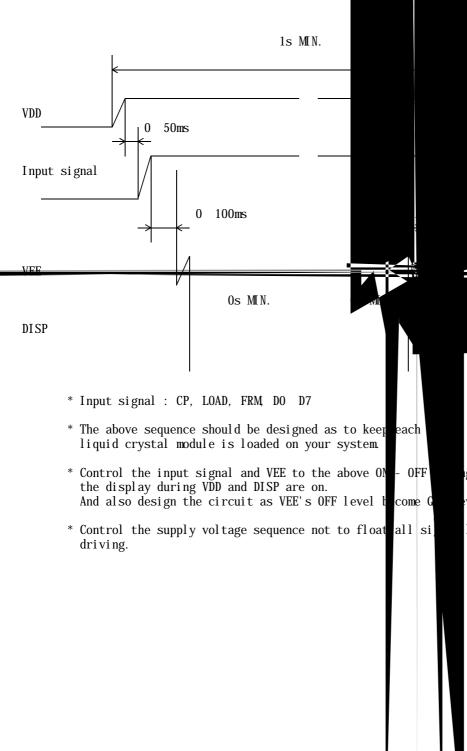


figure on condition that

ıg when you switch ON/OFF evel.

line when the LCD panel is



Backlight Characteristics



Temp. = 25

ITEM		SYMBOL	MIN.	TYP.	MAX.	NOTE
Starting discharge Voltage	*1	VS			790 Vrms.	0
	1	VS			525 Vrms.	25
Discharging tube current	*2, *3	IL	2.0 mArms.	5.0 mArms.	6.0 mArms.	
Discharging tube voltage		VL		320 Vrms.		
Operating life (IL=5.0 mArms.)	*4	Т	25,000 h	40,000 h		
Operating frequency		F	30 kHz		100 kHz	

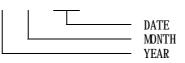
- *1 The Non-load output voltage (VS) of the inverter should be designed to have some margin, because VS may increase due to the leak current which may becaused by wiring of CFL cables. (Reference value: 1030 Vrms MIN.)
- *2 We recommend that you should set the discharging tube current at lower than typical value so as to prevent the heat accumulation of CFL tube from deteriorating a performance of the LCD.
- *3 Do not apply more than 6.0mA discharging tube current. Because CFL maybe broken due to over current.
- *4 When the illuminance or quantity of light has decreased to 50% of the initial value. Average life time of CFL will be decreased when LCD is operating at lower and higher temperature.





Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.



YEAR	2004	2005	2006	2007	2008	2009
CODE						

MONTH	JAN.	FEB.	MAR.	APR.	MAY	JUN.
CODE						

MONTH	JUL.	AUG.	SEP.	OCT.	NOV.	DEC.
CODE						

Warranty

15-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

15-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.





Precautions for use

16-1. Installation of the LCD

- 1. Please ground either of the mounting(screw) holes located at each corner of an LCD module, in order to stabilize brightness and display quality.
- 2. A transparent protection plate shall be added to protect the LCD and its polarizers.
- 3. The LCD shall be installed so that there is no pressure on the LSI chips.
- 4. The LCD shall be installed flat, without twisting or bending.
- 5. The display window size should be the same as the effective viewing area.
- 6. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
- 7. Do not pull the CFL lead wires and do not bend the root of the wires. Housing should be designed to protect CFL lead wires from external stress.
- 8. This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.

16-2. Static Electricity

1. Since CMDS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operation should wear ground straps.

16-3. LCD Operation

- 1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2. Adjust "LCD driving voltage" to obtain optimum viewing angle and contrast.
- 3. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles. It may also change the characteristics of the liquid crystal. <u>This phenomenon may not recover</u>. The LCD shall be operated within the temperature limits specified.

16-4. Storage

- 1. The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protected the LCD from direct sunlight or fluorescentlight.
- 2. The LCD should be packaged to prevent damage.

16-5. Screen Surface

- 1. $\underline{\text{DO NOT}}$ store in a high humidity environment for extended periods. Image degradation, bubbles, and/or peeling off of polarizer may result.
- 2. The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3. The LCD screen may be cleaned with a soft cloth or cotton pad. Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed.
- 4. Water may cause damage or discoloration of the polarizer. Clean any condensation or moisture from any source immediately.
- 5. Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizers.





17 Reliability Data / Environmental Test

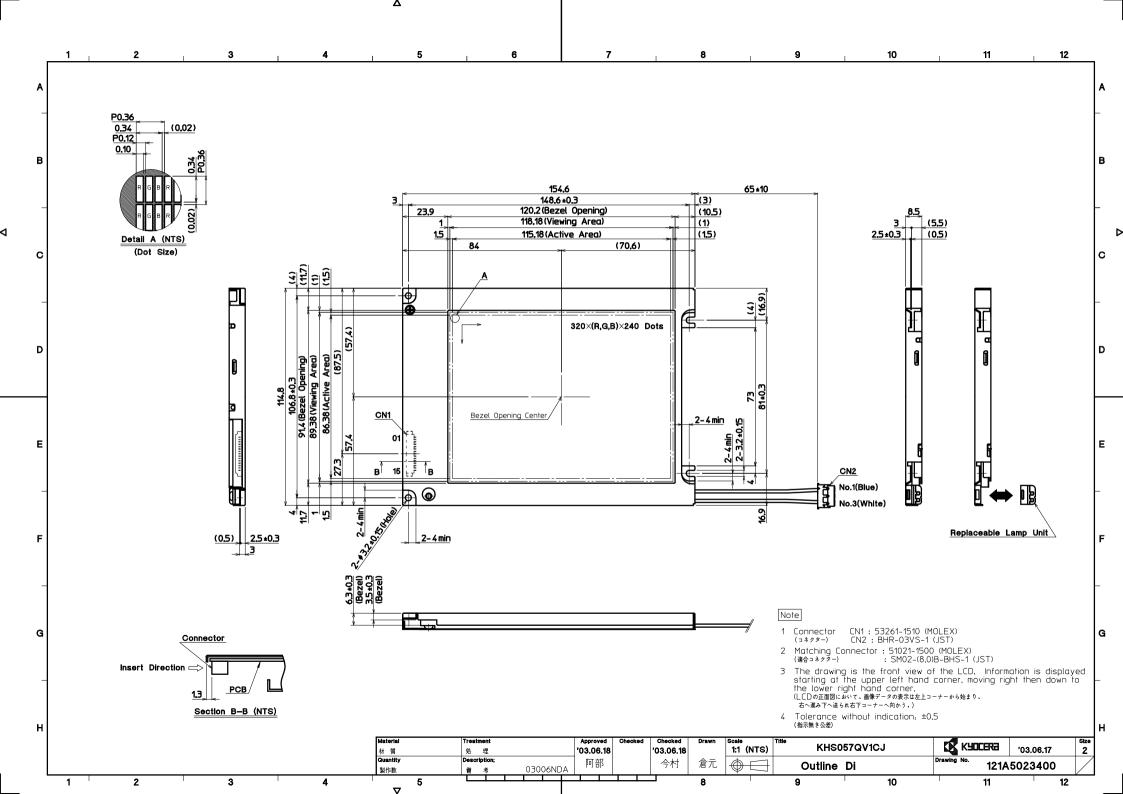
TEST ITEM	TEST CONDITION	TEST TIME	RESULT
High Temp. Atmosphere	60	240 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	- 20	240 h	Low Temp. Bubble : None Solid Crystallization of Liquid Crystal : None Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Humidity Atmosphere	40 90%RH	240 h	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect
Temp. Cycle	-20 0.5 h R.T. 0.5 h 60 0.5 h	10cycl es	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None
High Temp. Operation	50 Vop	500 h	Display Quality : No defect Current Consumption : No defect

* Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

 * The LCD is tested in circumstances in which there is no condensation.

 \ast The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.

- * The reliability test is not an out-going inspection.
- * The results of the reliability test are for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.







			SPEC.NO.	TQ3C-8EACO	-E2CWG24-00
			DATE	June 1	8, 2003
	<u>FOR:</u>				
<u>K Y O</u>	<u>CERA II</u>	N S P E C T I	<u>on stai</u>	<u>N D A R D</u>	
	<u> </u>	<u>HS057Q</u> V	<u>/ 1 C J – G 0</u>	<u>1</u>	
			KAG	CERA CORPORAT OSHIMA HAYATO DIVISION	
Original '	Designed by	:Engineering	Dept.	Confirmed by	v :QA Dept.
Issue Data	Prepared	Checked	Approved	Checked	Approved
June 18, 2003	W.Yand	M.FujiTani	H. ORno	y yoshida	D. Hayachi





		Designed	d by:	Engineering D		Confirmed by	: QA Dept.
Date	-	Prepare		Checked	Approved	Checked	Approved
		•			- *		- *
Rev. No.	Date	F	Page		Descripti	ons	1

Revision Record



Visuals specification



Item		Note				
General	 When defects specified in this Inspection Standards are inspected, operating voltage(Vop) shall be set at the level where optimized contrast is available. Display quality is applied up to effective viewing area. (Bi-Level INSPECTION) 					
	applied to any defect w	ed about the image quality shall be within the effective viewing area wable to outside of the area.				
	standard happen, additi	ch are not specified in this conal standard shall be determined cween customer and Kyocera.				
	4. Inspection conditions					
	Luminance : 500 Lux minimum . Inspection distance : 300 mm (from the sample) Temperature : 25 ± 5 Direction : right above					
Definition of Inspection item	Pinhole, Bright spot Black spot, Scratch Foreign particle	The color of a small area is different from the remainder. The phenomenon does not change with voltage.				
	Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage.				
	Polarizer (Scratch, Bubble, Dent)	Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.				





2)Standard

Inspection item		Ju	dgement	standard	
Pinhole, Bright spot Black spot, Foreign particle		a			
				d = (a +	b) / 2
	Category	Size	(mm)	Acceptab	le number
	А	d	0.2	neg	lected
	В	0.2 d	0.3		5
	С	0.3 d			3
	D	0.5 d			0
Scratch,Foreign particle	A B C 0.03 D	<pre>th (mm) W 0.03 W 0.1</pre>	Len 2.0 4.0	gth (mm) L 2.0 L 4.0 L	Acceptable No. neglected neglected 3 0
	E 0.1	W			According to Circular
Contrast variation	Category A B C	Size (d 0.5 d 0.7 d	0.5 0.7	neg	b) / 2 le number lected 3 0

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