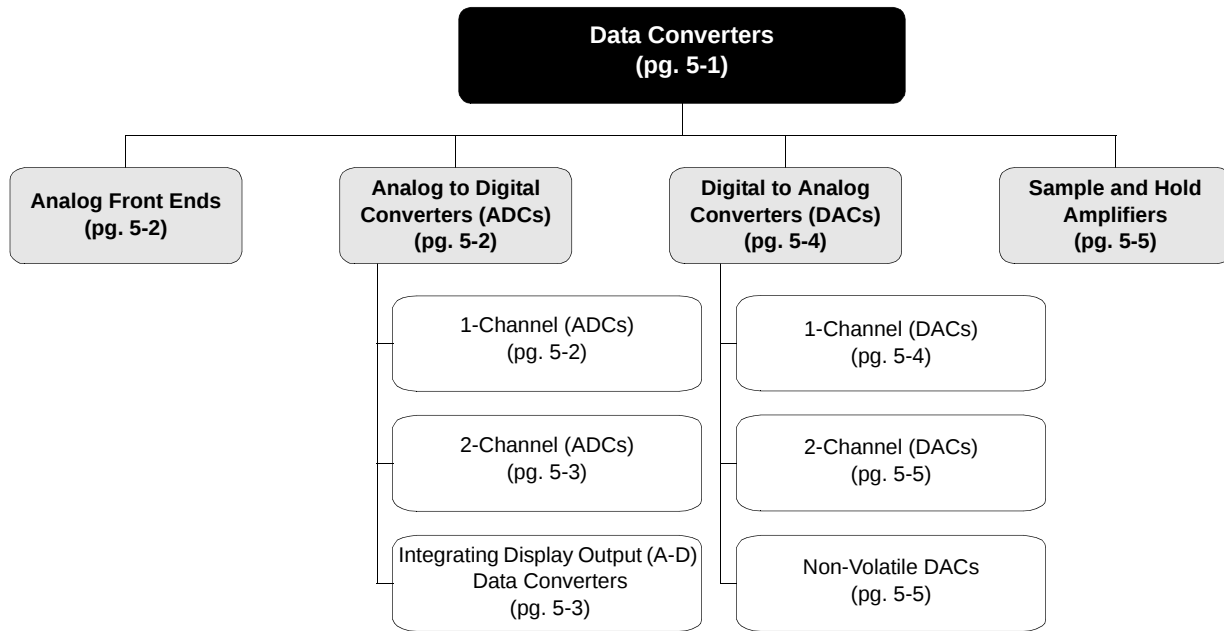




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Analog Front Ends

Device	Resolution (Bits)	Max Conversion Rate (MSPS)	Typical PLL Jitter (ps) (p-p)	Sync Measurement	Auto Phase Adjust	Typical Power Dissipation @ Max Conversion Rate (mW)	Package
ISL51002	10	110, 150, 165	250	Y	Y	940, 970, 980	128 Ld MQFP
ISL98001	8	140, 170, 210, 240, 275	250	N	N	950, 1050, 1100, 1150, 1200	128 Ld MQFP
ISL98002	8	140, 170	250	N	N	500	72 Ld QFN

Analog to Digital Converters (ADCs)

1-Channel (ADCs)

6-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Conv. Type	Input BW (MHz)	Input V_{IN} (Range) (V)	Power Supply (V_S)	INL (max) ($\pm$ LSB)	DNL (max) ($\pm$ LSB)	V_{REF}	Package
HI-5701	6	30	Flash	20	4	5	1.25	0.6	Ext	18 Ld SOIC

8-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Conv. Type	Input BW (MHz)	Input V_{IN} (Range) (V)	Power Supply (V_S)	INL (max) ($\pm$ LSB)	DNL (max) ($\pm$ LSB)	V_{REF}	Package
HI3276	8	160	Flash	250	2	+5, $\pm$ 5	0.5	0.5	Ext	48 Ld MQFP
HI5714	8	40, 75	2-Step Folding	18	2.7	5	0.75	0.4	Ext	24 Ld SOIC

10-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Conv. Type	Input BW (MHz)	Input V_{IN} (Range) (V)	Power Supply (V_S)	INL (max) ($\pm$ LSB)	DNL (max) ($\pm$ LSB)	V_{REF}	Package
HI5746	10	40	Pipeline	250	1	+5VA, +3VD	2	1	Ext	28 Ld SOIC, 28 Ld SSOP
HI5766	10	60	Pipeline	250	1	+5VA, +3VD	2	1	Ext	28 Ld SOIC, 28 Ld SSOP
HI5767	10	20, 40, 60	Pipeline	250	1	+5VA, +3VD	1.75	1	Int/Ext	28 Ld SOIC, 28 Ld SSOP

12-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Conv. Type	Input BW (MHz)	Input V_{IN} (Range) (V)	Power Supply (V_S)	INL (max) ($\pm$ LSB)	DNL (max) ($\pm$ LSB)	V_{REF}	Package
HI-574A	12	0.04	SAR		$\pm$ 10 or 0 to 20	$\pm$ 12 to $\pm$ 15	0.5		Int/Ext	28 Ld PDIP, 28 Ld SBDIP
HI-674A	12	0.067	SAR		$\pm$ 10 or 0 to 20	$\pm$ 12 to $\pm$ 15	0.5		Int/Ext	28 Ld PDIP, 28 Ld SBDIP
HI5805	12	5	Pipeline	100	4	+5VA, +3VD	2	1	Int	28 Ld SOIC
HI5812	12	0.05	SAR	1	0 to 5	+3 to +6	1	2	Ext	24 Ld PDIP, 24 Ld SOIC

16-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Conv. Type	Input BW (MHz)	Input V_{IN} (Range) (V)	Power Supply (V_S)	INL (max) ($\pm$ LSB)	DNL (max) ($\pm$ LSB)	V_{REF}	Package
HI7188	16	0.002	4th Order Sigma Delta		± 2.5	± 5			Ext	44 Ld MQFP

24-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Conv. Type	Input BW (MHz)	Input V_{IN} (Range) (V)	Power Supply (V_S)	INL (max) ($\pm$ LSB)	DNL (max) ($\pm$ LSB)	V_{REF}	Package
HI7190	24	0.002	2nd Order Sigma Delta		± 2.5	± 5			Ext	20 Ld PDIP, 20 Ld SOIC
HI7191	24	0.002	2nd Order Sigma Delta		± 2.5	± 5			Ext	20 Ld PDIP, 20 Ld SOIC

2-Channel (ADCs)

2x8-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Conv. Type	Input BW (MHz)	Input V_{IN} (Range) (V)	Power Supply (V_S)	INL (max) ($\pm$ LSB)	DNL (max) ($\pm$ LSB)	V_{REF}	Package
HI5662	2x8	60	Pipeline	250	1	+5VA, +3VD	1	0.5	Int/Ext	44 Ld MQFP

2x10-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Conv. Type	Input BW (MHz)	Input V_{IN} (Range) (V)	Power Supply (V_S)	INL (max) ($\pm$ LSB)	DNL (max) ($\pm$ LSB)	V_{REF}	Package
HI5762	2x10	60	Pipeline	250	1	+5VA, +3VD	2	1	Int/Ext	44 Ld MQFP

Integrating Display Output (A-D) Data Converters

Device	Resolution (Bits)	Conv. Rate (MSPS)	Conv. Type	Input BW (MHz)	Input V_{IN} (Range) (V)	Tech.	Power Supply (V_S)	INL (max) ($\pm$ LSB)	DNL (max) ($\pm$ LSB)	V_{REF}	Package
HI-7159A	5.5-Digits	0.2	2-Step	1.5	2.5 to -2.5	CMOS	± 5	0.5	0.5	Ext	28 Ld PDIP
ICL7106	3.5-Digits	3Hz (typ)	Integrating		± 0.2	CMOS	± 5			Int/Ext	40 Ld PDIP, 44 Ld MQFP
ICL7107	3.5-Digits	3Hz (typ)	Integrating		± 0.2	CMOS	± 5			Int/Ext	40 Ld PDIP, 44 Ld MQFP
ICL7126	3.5-Digits	3Hz (typ)	Integrating		± 0.2	CMOS	± 5			Int/Ext	40 Ld PDIP
ICL7135	4.5-Digits	2Hz (typ)	Integrating		± 0.2	CMOS	5			Int/Ext	28 Ld PDIP
ICL7136	3.5-Digits	3Hz (typ)	Integrating		± 0.2	CMOS	± 5			Int/Ext	40 Ld PDIP, 44 Ld MQFP

Digital to Analog Converters (DACs)

1-Channel (DACs)

8-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Output I/V	Power Supply (V _S)	INL (max) (±LSB)	DNL (max) (±LSB)	V _{REF}	Package
CA3338	8	50	V	5	1	1	Ext	16 Ld PDIP, 16 Ld SOIC
CA3338A	8	50	V	5	0.75	0.75	Ext	16 Ld PDIP, 16 Ld SOIC
HI3338	8	50	V	5	1	1	Ext	16 Ld SOIC
HI5660	8	60, 125	I	+3 to +5	0.5	0.5	Int	28 Ld SOIC, 28 Ld TSSOP

10-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Output I/V	Power Supply (V _S)	INL (max) (±LSB)	DNL (max) (±LSB)	V _{REF}	Package
HI5760	10	60, 125	I	+3 to +5	1	0.5	Int/Ext	28 Ld SOIC, 28 Ld TSSOP
ISL5757	10	260	I	3.3	0.5	0.5	Int/Ext	28 Ld SOIC, 28 Ld TSSOP
ISL5761	10	130, 210	I	3.3	0.5	0.5	Int/Ext	28 Ld SOIC, 28 Ld TSSOP

12-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Output I/V	Power Supply (V _S)	INL (max) (±LSB)	DNL (max) (±LSB)	V _{REF}	Package
AD7521	12	1	I	+5 to +15	2	Monotonic	Ext	18 Ld PDIP
HI-DAC80V	12	0.667	V	+12 to +15	0.5	0.75	Int/Ext	24 Ld PDIP
HI-565A	12	6.7	I	+12 to +15	0.25	0.5	Int/Ext	24 Ld SBDIP
HI5731	12	100	I	5, -5.2	1.5	1	Int/Ext	28 Ld PDIP, 28 Ld SOIC
HI5735	12	80	I	5, -5.2	1.5	1	Int/Ext	28 Ld SOIC
HI5860	12	130	I	+3 to +5	2	1	Int/Ext	28 Ld SOIC, 28 Ld TSSOP
ISL5857	12	360	I	3.3	1.25	1	Int/Ext	28 Ld SOIC, 28 Ld TSSOP
ISL5861	12	130, 210	I	3.3	1.25	1	Int/Ext	28 Ld SOIC, 28 Ld TSSOP

14-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Output I/V	Power Supply (V _S)	INL (max) (±LSB)	DNL (max) (±LSB)	V _{REF}	Package
HI5741	14	100	I	5, -5.2	1.5	1	Int	28 Ld SOIC
HI5960	14	125	I	+3 to +5	5	3	Int/Ext	28 Ld SOIC, 28 Ld TSSOP
ISL5957	14	260	I	3.3	5	3	Int/Ext	28 Ld SOIC, 28 Ld TSSOP
ISL5961	14	130, 210	I	3.3	5	3	Int/Ext	28 Ld SOIC, 28 Ld TSSOP

2-Channel (DACs)

2x8-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Output I/V	Power Supply (V _S)	INL (max) (±LSB)	DNL (max) (±LSB)	V _{REF}	Package
HI5628	2x8	60, 125	I	+3 to +5	0.5	0.5	Int/Ext	48 Ld MQFP
ISL5627	2x10	260	I	3.3	0.5	0.5	Int/Ext	48 Ld LQFP
ISL5629	2x8	130, 210	I	3.3	0.5	0.5	Int/Ext	48 Ld MQFP

2x10-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Output I/V	Power Supply (V _S)	INL (max) (±LSB)	DNL (max) (±LSB)	V _{REF}	Package
HI5728	2x10	60, 125	I	+3 to +5	1	0.5	Int/Ext	48 Ld MQFP
ISL5727	2x10	260	I	3.3	0.5	0.5	Int/Ext	48 Ld LQFP
ISL5729	2x10	130, 210	I	3.3	0.5	0.5	Int/Ext	48 Ld MQFP

2x12-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Output I/V	Power Supply (V _S)	INL (max) (±LSB)	DNL (max) (±LSB)	V _{REF}	Package
HI5828	2x12	125	I	+3 to +5	0.75	0.5	Int	48 Ld MQFP
ISL5827	2x12	260	I	3.3	1.25	1	Int/Ext	48 Ld LQFP
ISL5829	2x12	130, 210	I	3.3	1.25	1	Int/Ext	48 Ld MQFP

2x14-Bit

Device	Resolution (Bits)	Conv. Rate (MSPS)	Output I/V	Power Supply (V _S)	INL (max) (±LSB)	DNL (max) (±LSB)	V _{REF}	Package
ISL5927	2x13	260	I	3.3	5	3	Int/Ext	48 Ld LQFP
ISL5929	2x14	130, 210	I	3.3	5	3	Int/Ext	48 Ld MQFP

Non-Volatile DACs

Device	Resolution (Bits)	Conv. Rate (MSPS)	Output I/V	Tech.	Power Supply (V _S)	INL (max) (±LSB)	DNL (max) (±LSB)	V _{REF}	Package
X79000	12	0.417	V	CMOS	5.5	10	0.5	Int/Ext	20 Ld TSSOP
X79001	12	0.417	V	CMOS	5.5	10	0.5	Int/Ext	20 Ld TSSOP

Sample and Hold Amplifiers

Device	BW (MHz)	SR (V/μs)	I _{BIAS} (mA)	I _S /Amp (mA)	A _{OL} (dB)	V _{OS} (V/μs) (mV)	EXT# Hold Cap#	t _{ACQ} (ns)	Droop Rate (μV/μs)	Hold Step Error (mV)	Package
HA-2420	2.5	5	40	3.5	94	2	Y	3200	0.005	10	14 Ld CerDIP
HA-2425	2.5	5	40	3.5	94	3	Y	3200	0.005	10	14 Ld PDIP
HA-5320	2	45	70	11	126	0.2	Y	1000	0.08	5	14 Ld CerDIP, 14 Ld PDIP, 16 Ld SOIC
HA-5330	4.5	90	20	19	146	0.2	N	650	0.01	0.5	14 Ld CerDIP
HA5351	40	105	2500	20	108	1	N	64	0.3	1	8 Ld SOIC

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